DATA CONVERTERS WITH

TERNARY PULSE WIDTH MODULATION OUTPUT STAGES

AND METHODS AND SYSTEMS USING THE SAME

ABSTRACT:

[0056]   A pulse width modulator includes at least one input for receiving an
input signal and pulse width modulation circuitry for generating a pulse width
modulated stream and another pulse width modulated stream. The pulse width
modulated stream and the another pulse width modulated stream are nominally out
of phase and together represent the received input signal. A summer sums the pulse
width modulated stream and the another pulse width modulated stream to generate
an analog output signal.

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